

6886,162

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PATENT

Atty. Docket No.: BEA919970012US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re application of: McKenney

SERIAL NO.: 09/127,085

FILING DATE: July 31, 1998

FOR: High Speed Method For  
Maintaining A Summary Of Thread  
Activity For Multiprocessor  
Computer Systems

Group Art Unit: 2151

Examiner: Banankhah, M.

Certificate  
MAY 13 2005

Certificate of Correction Transmittal Letter

of Correction

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313

Sir:

CERTIFICATION OF MAILING

I hereby certify that this correspondence and the documents indicated therein are being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313, on May 9, 2005.

5/9/2005  
Date of Deposit

Rochelle Lieberman

Enclosed please find a request for a Certificate of Correction for the above-identified U.S. Patent. In view of the fact that the errors on the request are not those of the Applicant, the Applicant's Attorney hereby requests that a Certificate of Correction be issued.

If any questions should arise in relation to the request herein, please contact Applicant's Attorney listed below.

Respectfully submitted,

By:

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Date: May 9, 2005

MAY 16 2005

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,886,162  
DATED : April 26, 2005  
INVENTOR(S) : McKenney

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Specification, Col. 9, line 44 should read:  
a callback 100 containing the element to next generation 110.

In the Specification, Col. 9, line 54 should read:  
store conditioned (SC) instructions, IBM RS6000 cache-

In the Specification, Col. 9, line 67 should read:  
thread accesses an element when it is erased.

In the Specification, Col. 14, line 41 should read:  
Rcc\_quad\_ctrblk is a per-node data structure that includes

In the Specification, Col. 14, line 47 should read:  
pq\_rcc\_curgen is a variable that contains a local genera-

In the claims, Claim 4, line 6 should read:  
processor indicate in the first data structure that is has passed

In the claims, Claim 6, line 4 should read:  
state, having a callback processor determine if there are

**MAILING ADDRESS OF SENDER:**

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PATENT NO. 6,886,162

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**MAY 16 2005**